

HP-41C Virtual Memory Routine by John Kennedy
PPC Calculator Journal V8 N6 P64 Aug-Dec 1981
PPC ROM is required for M4, PR, & UR routines

```
01 LBL A      ;Virtual Memory Routine
02 XROM 20,35 ;M4 - PPC ROM Matrix, Register Address to (i,j)
03 X<>Y
04 STO 11
05 RDN
06 XROM 20,45 ;PR - PPC ROM Pack Register
07 RTN
08 LBL B
09 XROM 20,35 ;M4 - PPC ROM Matrix, Register Address to (i,j)
10 X<>Y
11 STO 11
12 X<>Y
13 XROM 20,44 ;UR - PPC ROM Unpack Register
14 RTN
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0017CF66A523713B75A52D85CF67A523713B71A52C85C2000D
CC

23 BYTES

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Program Registers Needed: 4

Row 1 (1 - 9)



Row 2 (9 - 15)

